



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| AMENDMENT  Commissioner for Patents |  |   |                       | ER 280           |             |        |
|-------------------------------------|--|---|-----------------------|------------------|-------------|--------|
|                                     |  |   |                       | ECHNOLOGY CENTER | OCT 23 2003 | CEIVED |
|                                     |  | ) | ÷ .                   | CHMOLO           | . OCT       | REC    |
|                                     | DEVICE                                     | ) | October 16, 2003      | 12               |             |        |
|                                     | THEREOF AND ELECTRONIC                     | ) |                       |                  |             |        |
| For:                                | SEMICONDUCTOR DEVICE, MANUFACTURING METHOD | ) |                       |                  |             |        |
| Filed:                              | d: August 30, 2000                         |   | Examiner: D. Farahani |                  |             |        |
| Serial No. 09/651,889               |  | ) | Group Art Unit: 2814  | 4                |             |        |
| Ritsuko KAWASAKI et al.             |  | ) |                       |                  |             |        |
| In re I                             | Patent Application of:                     | ) |                       |                  |             |        |

P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to the Examiner's Office Action dated May 16, 2003 and to the Examiner interview conducted September 30, 2003, please consider the following amendments and remarks in connection with the above-identified application.